Draw the output waveform of each device below, assuming no propagation delay. As shown in the lower diagram, signal IN_1 is used as the clock/enable for the flip-flop and latch, and IN_2 is used as the D input. Both outputs are initially low.

IN₂ —| D   Q |—
IN₁ —|▷ |

IN₂ —| D   Q |—
IN₁ —| en |