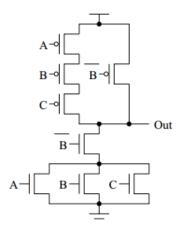
Derive the Boolean expression for the following CMOS circuit:



$$Out = \overline{A} \cdot \overline{B} \cdot \overline{C} + B$$

(which can be simplified, but the question asks for the expression for the CMOS, not a logically-equivalent expression)