

- a) Design a switch network using only N-FETs that creates a connection when either (or both) of inputs A or B is high, but only if input C is low. Label FET gates with complemented input signals if needed.
- b) Design another switch network using only P-FETs that creates a connection under the same conditions as the network in part a.
- c) Design another switch network using only P-FETs that creates a connection under the *opposite* conditions as the network in part a.