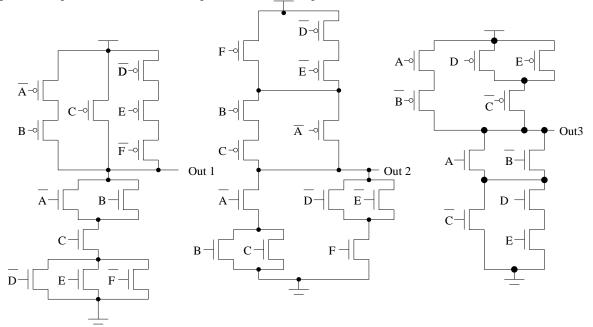
## Understanding Transistor Design

For part A, complete the circuit by adding NFETs and determine the expression. For part B, complete the circuit by adding PFETs and determine the expression. For part C, implement the circuit for the specified expression. Assume the inputs and their complements are available.



 $Out_1 = A\,\overline{B} + \overline{C} + D\,\overline{E}\,F$ 

 $Out_2 = (A + \overline{B} \overline{C})(D E + \overline{F})$ 

 $Out_3 = \overline{A} B + C(\overline{D} + \overline{E})$