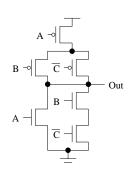
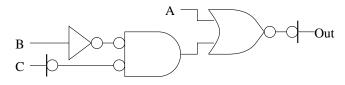
Logic Design

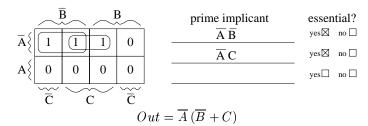
Part A



Part B



Part C



Part D

