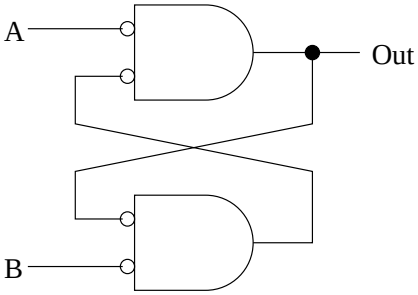
Transparent Latches

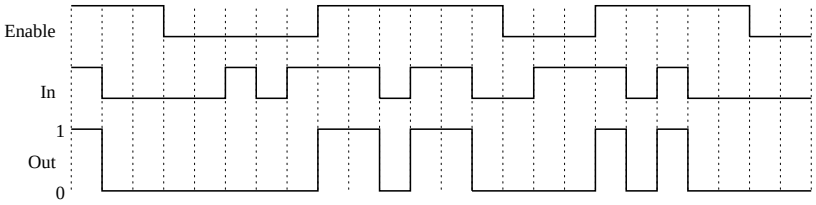
Part A



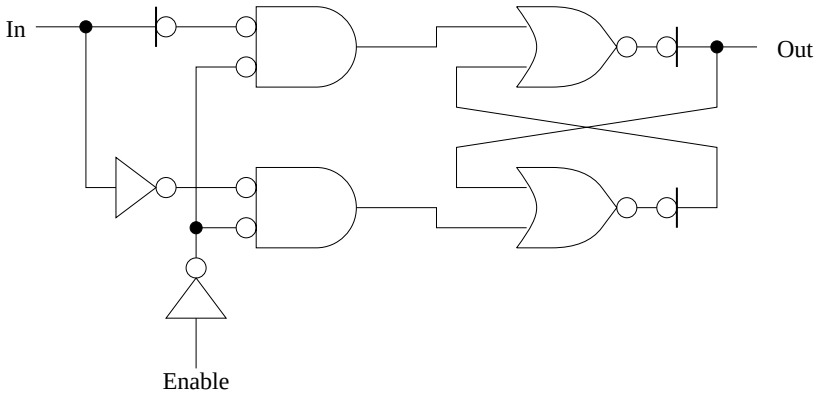
A B	Out
0 0	Qo
1 0	0
0 1	1
1 1	0 (unstable)

Part B $(2 \text{ x } 4) + (2 \text{ x } 6) + (1 \text{ x } 2) = 22$ transistors

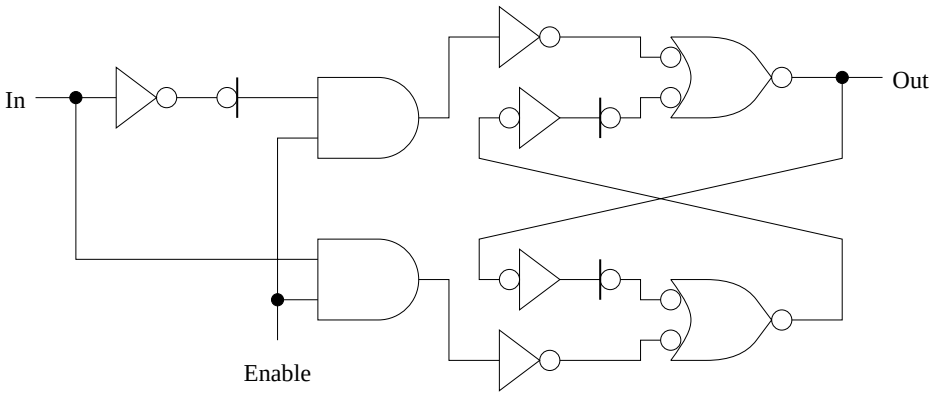
Part C



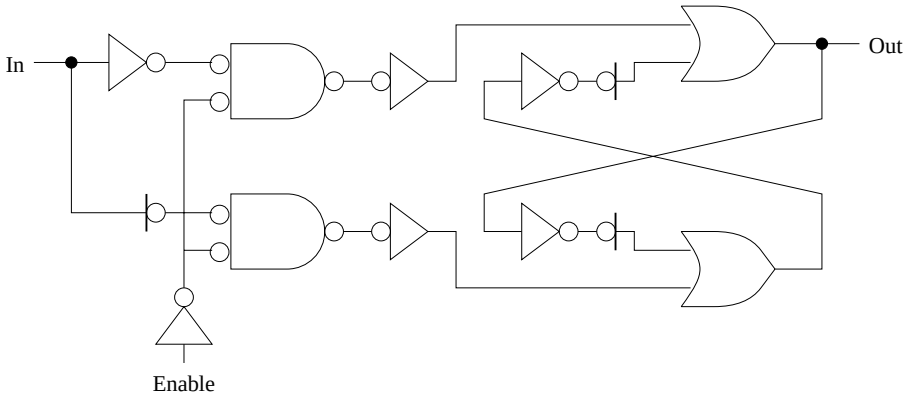
Part D



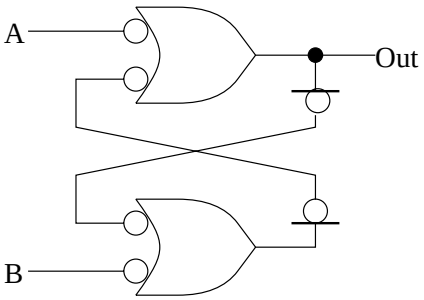
Part E



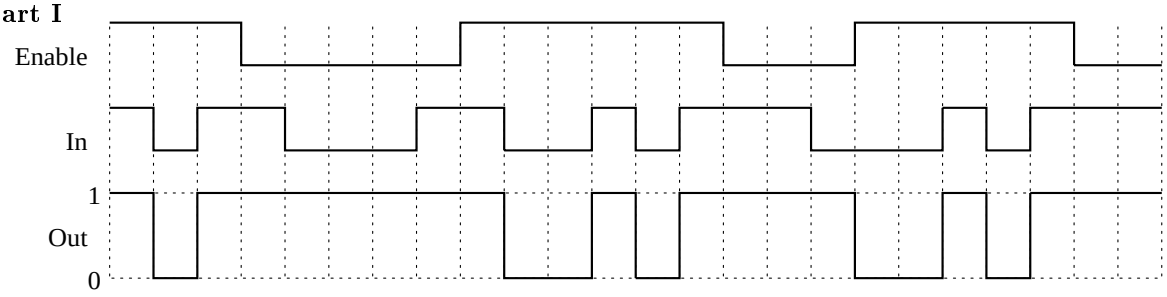
Part F



Part G



A B	Out
0 0	1
1 0	0 reset
0 1	1 set
1 1	Q_0 hold

Part H 4 x 4 + 1 x 2 = 18 transistors

Part I