

Write a microcode fragment that computes the average of four packed eight bit values stored at memory address 1000. Leave the result in register 0.

<i>cycle</i>	<i>X</i>	<i>Y</i>	<i>Z</i>	<i>rwe</i>	<i>im en</i>	<i>im va</i>	<i>au en</i>	<i>-a/s</i>	<i>lu en</i>	<i>lf</i>	<i>su en</i>	<i>st</i>	<i>ld en</i>	<i>st en</i>	<i>r/-w</i>	<i>msel</i>	<i>description</i>
1	X	X	0	1	1	1000	0	X	1	C	0	X	0	0	X	0	R0 ← 1000
2	0	X	0	1	0	X	0	X	0	X	0	X	1	0	1	1	R0 ← (R0)
3	0	X	1	1	1	FF	0	X	1	8	0	X	0	0	X	0	R1 ← R0 and FF
4	0	X	0	1	1	8	0	X	0	X	1	0	0	0	X	0	R0 ← R0 lsf 8
5	0	X	2	1	1	FF	0	X	1	8	0	X	0	0	X	0	R2 ← R0 and FF
6	1	2	1	1	0	X	1	0	0	X	0	X	0	0	X	0	R1 ← R1 + R2
7	0	X	0	1	1	8	0	X	0	X	1	0	0	0	X	0	R0 ← R0 lsf 8
8	0	X	2	1	1	FF	0	X	1	8	0	X	0	0	X	0	R2 ← R0 and FF
9	1	2	1	1	0	X	1	0	0	X	0	X	0	0	X	0	R1 ← R1 + R2
10	0	X	0	1	1	8	0	X	0	X	1	0	0	0	X	0	R0 ← R0 lsf 8
11	1	0	1	1	0	X	1	0	0	X	0	X	0	0	X	0	R1 ← R1 + R0
12	1	X	0	1	1	2	0	X	0	X	1	1	0	0	X	0	R0 ← R1 asf 2