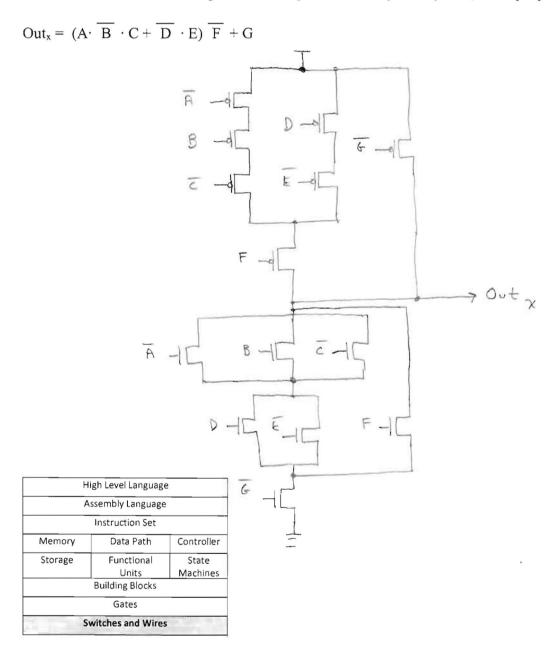
June 4, 2014

Name: ___ · KEY

6 pages, 100 possible points. Show your work for any possible partial credit. No calculators, no cell phones allowed.

Switch Level Circuits:

1) (15 total point) For the expression below, create a switch level implementation using N and P type switches. Assume both inputs and their complements are available. Your design should contain no shorts or floats. Implement the equations exactly as they are (no simplifying).



Switch-Ready Expressions:

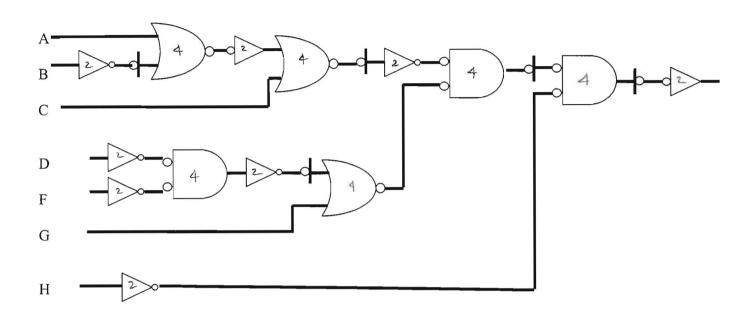
2) (15 points) Transform each of the following Boolean expressions to a form where they are ready for switch level implementation (i.e., there should only be bars over input variables, not over operations). The behavior of the expression should remain unchanged. **Do not implement**, just show the new Boolean equation without any "big bars". (Note: The line above the boolean equation is a bar and is not a blank for you to write an answer in).

Outx =
$$\overline{(D \cdot E + F \cdot G) \cdot H} + \overline{I \cdot J}$$

 $\overline{(D \cdot E) + (F \cdot G) + H} + \overline{I} + \overline{J}$
 $\overline{(D \cdot E) + (F \cdot G) + H} + \overline{I} + \overline{J}$
 $\overline{(D \cdot E) \cdot (F \cdot G) + H} + \overline{I} + \overline{J}$
 $\overline{(D \cdot E) \cdot (F \cdot G) + H} + \overline{I} + \overline{J}$
 $\overline{(D + E) \cdot (F + G) + H} + \overline{I} + \overline{J}$
 $\overline{(D + E) \cdot (F + G) + H} + \overline{I} + \overline{J}$

Hi	gh Level Languag	е
A	ssembly Language	9
	Instruction Set	
Memory	Data Path	Controller
Storage	Functional	State
	Units	Machines
	Building Blocks	_
	Gates	
Sı	witches and Wire	s

3) (15 points) Write the boolean output expression for the gate design shown below. Also determine the number of switches used in its implementation.

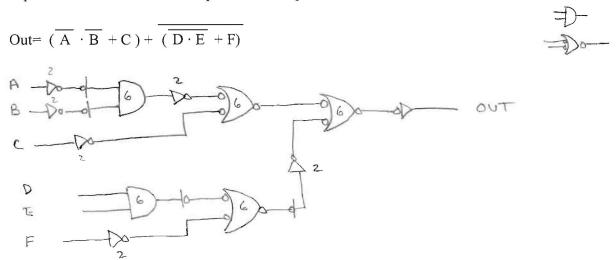


Out
$$A + B + C \cap D \cdot F + G \cdot H$$

number of switches $6\times4+8\times2=46$

As	sembly Language	2
	Instruction Set	_
Memory	Data Path	Controlle
Storage	Functional Units	State Machines
	Building Blocks	
	Gates	
Sv	vitches and Wire:	5

4) (15 points) Implement the following expression using only two input AND gates and inverters so as to minimize the number of switches required. Then determine the number of switches required. **Use proper mixed logic notation**. Do not modify the expression, do not simplify the expression. Do not assume complements of inputs are available.



Number of switches

5x6 + 7x2 = 44

Hi	gh Level Languag	е
A:	ssembly Language	9
	Instruction Set	
Memory	Data Path	Controller
Storage	Functional	State
	Units	Machines
	Building Blocks	
	Gates	
Sı	witches and Wire	s

5) (15 points) Determine the canonical sum of products (using minterms) expressions for the truth table below:

A	В	C	OUT
0	0	0	1
0	0	1	0
0	1	0	1
0	1	1	0
1	0	0	1
1	0	1	0
1	1	0	0
1	1	1	0

SOP (minterms) =
$$\overline{A \cdot B \cdot C} + \overline{A} \cdot B \cdot \overline{C} + \overline{A} \cdot B \cdot \overline{C}$$

	gh Level Languag	e
A	ssembly Language	2
	Instruction Set	
Memory	Data Path	Controller
Storage	Functional	State
	Units	Machines
	Building Blocks	
	Gates	
Si	witches and Wire:	5

Karnaugh Maps:

6) (15 points) Part A: For the following expression, derive a simplified *sum of products* expression using a Karnaugh Map. Circle and list **ALL** the prime implicants, indicating which are essential.

 $\overline{A} \overline{B} \overline{C} \overline{D} + \overline{A} \overline{B} \overline{C} \overline{D} + \overline{A} \overline{B} \overline{C} D + \overline{A} \overline{B} \overline{C} \overline{D} + \overline{A} \overline{B} \overline{C} \overline{D}$

	Ē	-	E	131			prime implicants	esser yes	ntial? no
-		\sim				í	BCD	\searrow	
2	D	O	0	U	C	2	AZD	\times	
\overline{A}			_	0		3	BED		\times
(0		٥	G	\sc	4	ABD		X
	0	1 5	0	6	5	5	ACD		X
A \		0	٥	W	<u></u>	6	ABC		7
(,		M			-		
	D		\sim	101			Section 14		
			-						

Simplified sum of products

Part B: (10 points) There may be more than one correct answer. For full credit, show **ALL** additional possible correct simplified sum of products answers (if there are any). Show your work!

