Instructions: This is a closed book, closed note exam. Calculators are not permitted. If you have a question, raise your hand and I will come to you. Please work the exam in pencil and do not separate the pages of the exam. For maximum credit, show your work.
Good Luck!

Your Name (please print) $\qquad$


Problem 1 (3 parts, 30 points)
Switch-level Design
Part A (24 points) For each expression below, create a switch level implementation using N and P type switches. Assume both inputs and their complements are available. Your design should contain no shorts or floats. Use as few transistors as possible.

$$
\text { OUTx }=\bar{A} \cdot B \cdot(C+\bar{D})+E \cdot \bar{F} \cdot G
$$

$$
\text { OUTy }=\overline{(X+\bar{Y} \cdot \bar{Z}+W) \cdot(\bar{P}+Q \cdot R) \cdot S}
$$

Part B (6 points) Given the pull-down network below, draw the missing pull-up network so that the circuit contains no floats or shorts. Also, write the Boolean expression computed by the completed circuit. (The expression should have complements only over the individual input signals, not over subexpressions.)

$O u t_{Z}=$

Part A (5 points) Transform the following Boolean expression to a form where it can be implemented using switches (i.e., there should only be bars over input variables, not over operations). The behavior of the expression should remain unchanged. Do not implement.

$$
\text { Out }_{A}=\overline{\bar{A}} \cdot \overline{\bar{B}} \cdot \bar{C}+D \cdot \overline{\bar{E} \cdot \bar{F} \cdot G}
$$

$$
\text { Out }_{A}=
$$

Part B (6 points) Derive a canonical sum of products (using minterms) and a product of sums (using maxterms) expression for the truth table below.

| A | B | C | $\mathrm{F}_{(\mathrm{A}, \mathrm{B}, \mathrm{C})}$ |
| :---: | :---: | :---: | :---: |
| 0 | 0 | 0 | 0 |
| 1 | 0 | 0 | 1 |
| 0 | 1 | 0 | 1 |
| 1 | 1 | 0 | 0 |
| 0 | 0 | 1 | 1 |
| 1 | 0 | 1 | 0 |
| 0 | 1 | 1 | 1 |
| 1 | 1 | 1 | 1 |

Standard SOP (MINTERMS) $=$
Standard POS (MAXTERMS) $=$
Part C (9 points) Translate the following standard sum of products (which uses minterms) to the equivalent standard product of sums (using maxterms) by first filling in the truth table below and then deriving the standard product of sums from it.
${\text { Standard } \operatorname{SOP}_{\text {(MINTERMS) }}=}_{\bar{A} \bar{B} C+A \bar{B} C+A \bar{B} \bar{C}+A B \bar{C}+A B C}$

| A | B | C | $\mathrm{F}_{(A, B, C)}$ |
| :---: | :---: | :---: | :---: |
| 0 | 0 | 0 |  |
| 1 | 0 | 0 |  |
| 0 | 1 | 0 |  |
| 1 | 1 | 0 |  |
|  | 0 | 0 | 1 |
|  |  |  |  |
| 1 | 0 | 1 |  |
| 0 | 1 | 1 |  |
| 1 | 1 | 1 |  |

Problem 3 (3 parts, 30 points)
Mixed Logic Reengineering


Part A (10 points) Write the output expression for the gate design shown above. Also determine the number of switches used in its implementation.

OUTx $=$ $\qquad$
OUTy = $\qquad$
\# switches = $\qquad$
Part B (10 points) Reimplement the behavior below with a mixed logic design style using only NAND gates and inverters. Determine the number of switches used in this implementation.
$\qquad$

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Part C (10 points) Implement the following expression using only AND gates and inverters. Then determine the number of transistors required. Use proper mixed logic notation. Do not modify the expression. Do not assume complements of inputs are available.

$$
\text { Out }_{Z}=(\bar{A}+\bar{B} \cdot C) \cdot \overline{\bar{B}} \cdot C \cdot D
$$

\# switches =

Problem 4 (2 parts, 20 points)
Karnaugh Maps
Part A (10 points) Given the following Karnaugh Map, circle and list all the prime implicants, indicating which are essential and write the simplified sum-of-products (SOP) expression.

simplified $S O P$ expression
Part B (10 points) Simplify the following SOP expression using a Karnaugh Map. Circle and list all the prime implicants, indicating which are essential and write the product-of-sums (POS) expression.

$$
\text { Out }=\bar{A} \bar{B} C+A \bar{B} \bar{C}+A \bar{B} C
$$



Simplified $P O S$ expression $\qquad$

